CIRCUIT SIMULATOR SYSTEM AND METHOD

Abstract of the Disclosure

A transistor-level simulator system and method which uses a simulator=s API=s construct a circuit code module which is used to perform circuit simulation of input and output wave forms without the user having to provide a detailed netlist. This circuit simulation tool may be used on more complex gates where other models may be impractical to obtain without loss in the fidelity of the waveforms.

Figures